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By: [Signature] Date: October 3, 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Rainald Sander
Appl. No. : 09/943,589
Filed : August 30, 2001
Title : Circuit Arrangement to Determine the Current in a Load Transistor
Art Unit :

LETTER

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

The above-mentioned new patent application was filed on August 30, 2001 without a signed oath or declaration, under the provision of 37 C.F.R. 1.53(f), and in a language other than English.

In accordance with the above-mentioned rule, enclosed herewith are the original signed declaration and certified English translation.

The undersigned hereby states that the application filed in the Patent and Trademark Office is the application which the inventor(s) executed by signing the declaration. MPEP 601.01(a).

The fee required for the late filing of an oath or declaration in the amount of \$130.00 is also enclosed, along with the fee of \$130.00 for the late filing of a certified English translation.

Respectfully submitted,

[Signature]
GREGORY L. MAYBACK
REG NO. 40,719

/mjb

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